



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Steven H. Voldman

Serial No.: 10/604,176

Filed: June 30, 2003

Title: A Method, Apparatus and Circuit for

Latchup Suppression In a Gate-Array

ASIC Environment

IBM Docket No.:

BUR920030009US1

(02016-00151)

Group Art Unit: Not yet assigned

Examiner: Not yet assigned

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

CERTIFICATE OF MAILING (37 C.F.R. 1.8a)

I hereby certify that this correspondence is, on the date shown below, being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the

Commissioner for Patents, Alexandria, VA 22313-

1450

Anthony P. Gangerni

July 7, 2003

Supplemental Information Disclosure Statement Transmittal

In connection with the above-identified application, please find attached a Supplemental Information Disclosure Statement and copies of the references cited therein.

Respectfully submitted,

DOWNS RACHLIN MARTIN PLLC

Bv:

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BTV/245689.1

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